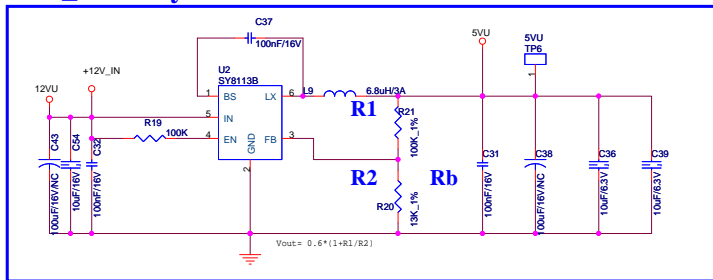
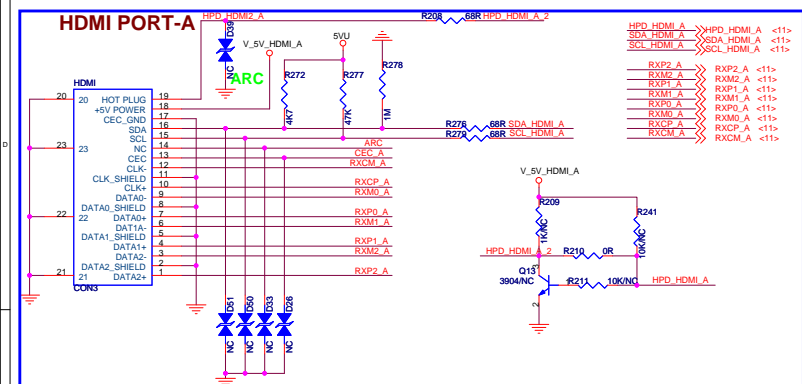


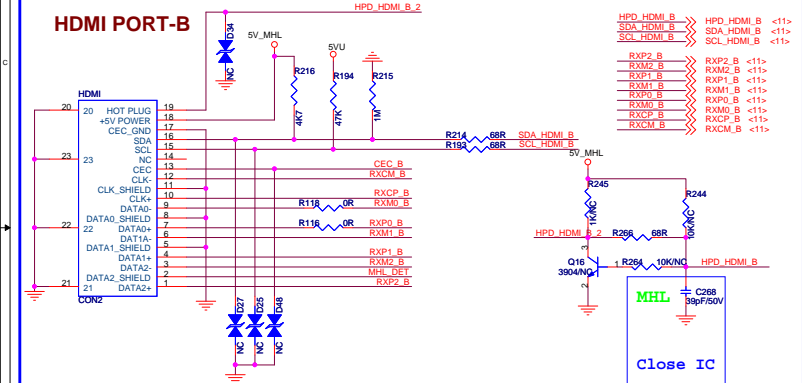
+5V_Standby POWER 3A



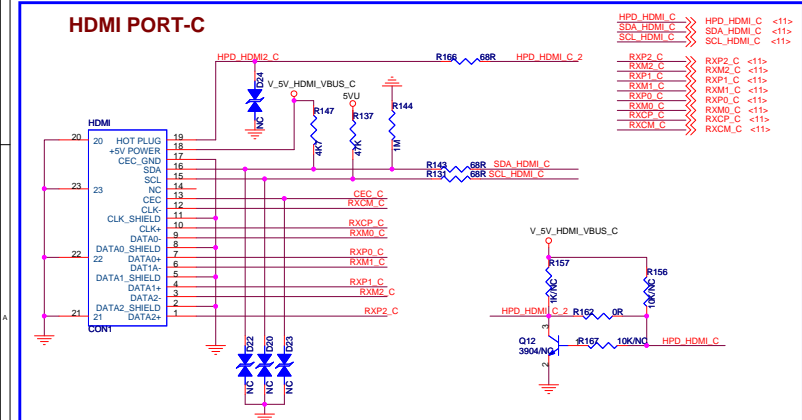
HDMI PORT-A



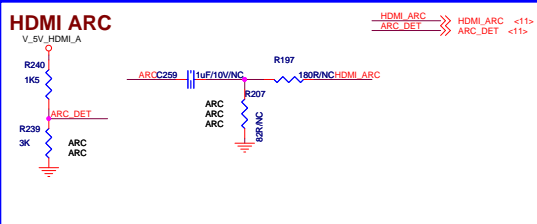
HDMI PORT-B



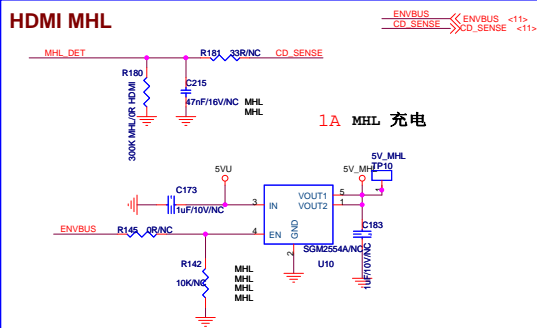
HDMI PORT-C



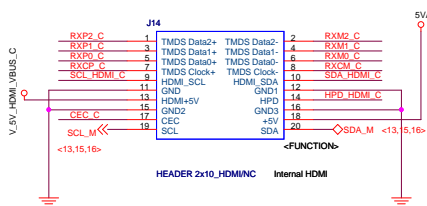
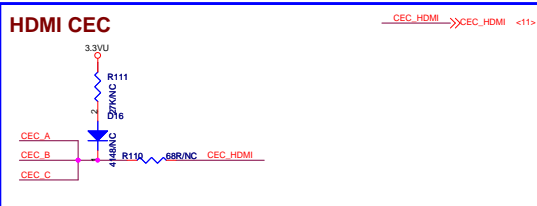
HDMI ARC



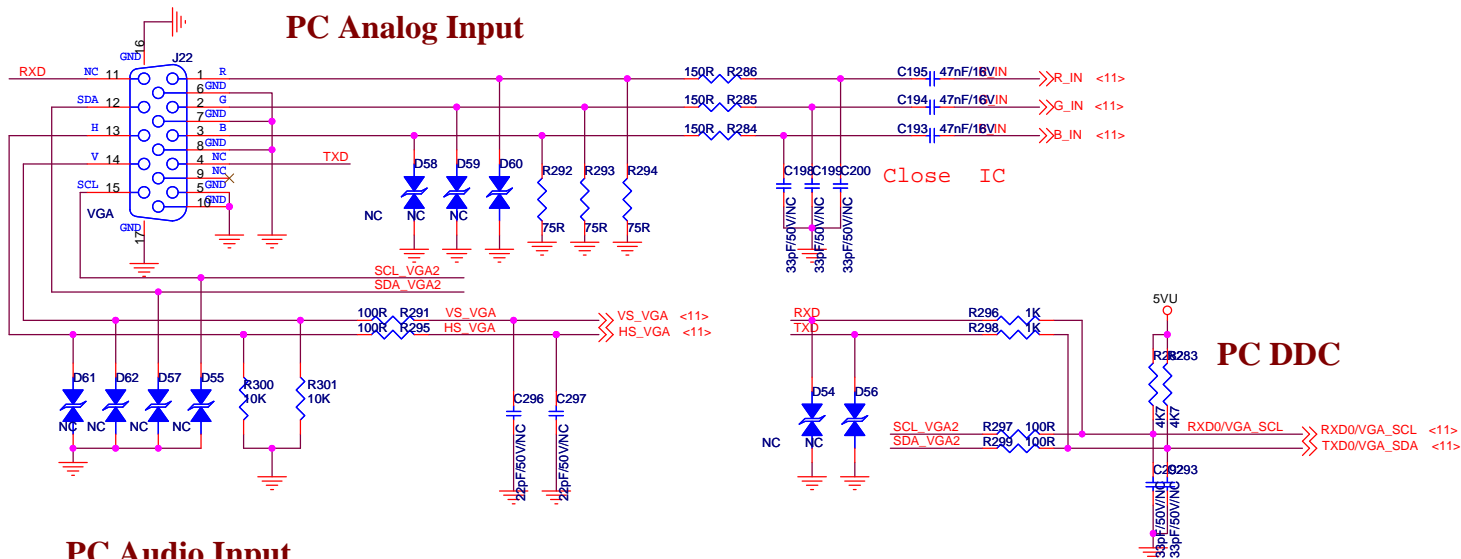
HDMI MHL



HDMI CEC

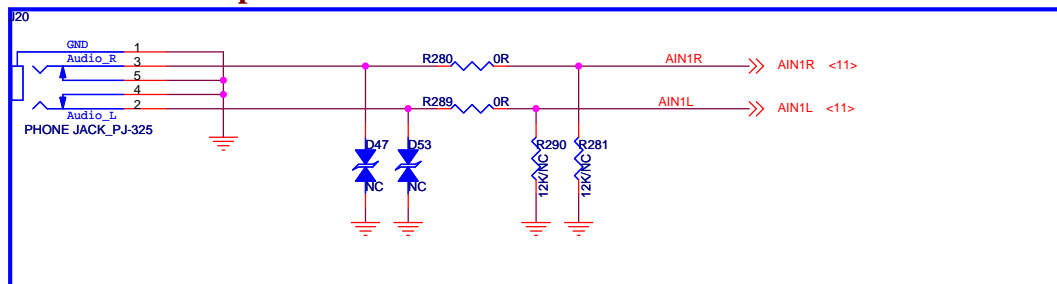


PC Analog Input

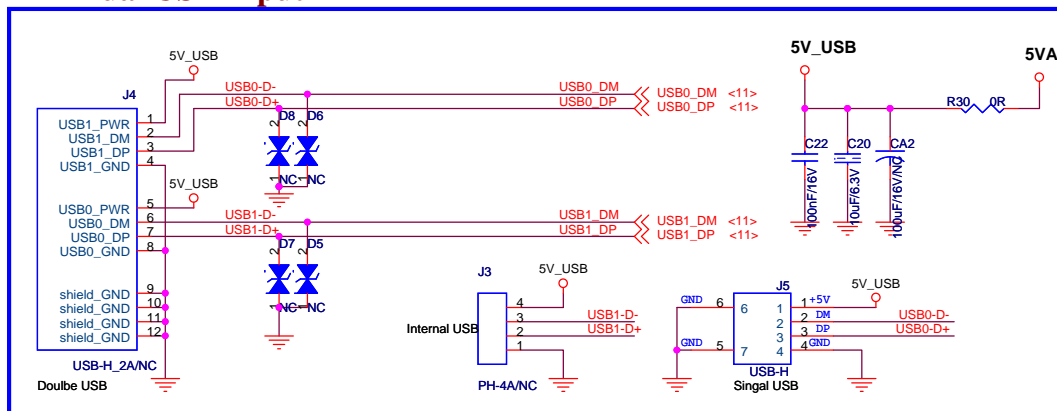



PC DDC

PC Audio Input

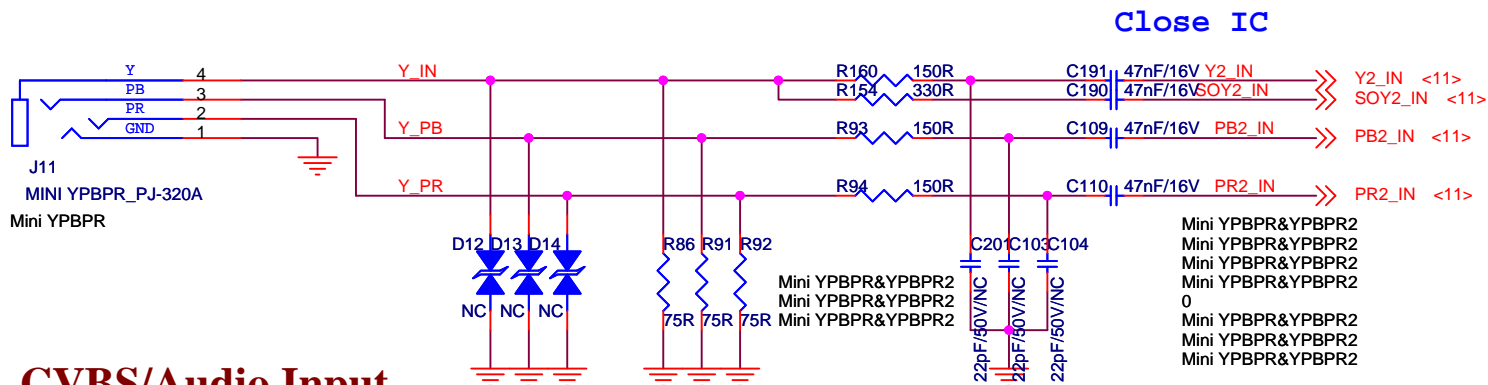


Dual USB Input

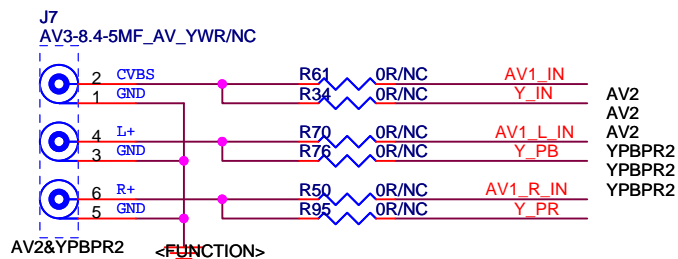


		Title CV512H-A50	
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Custom			
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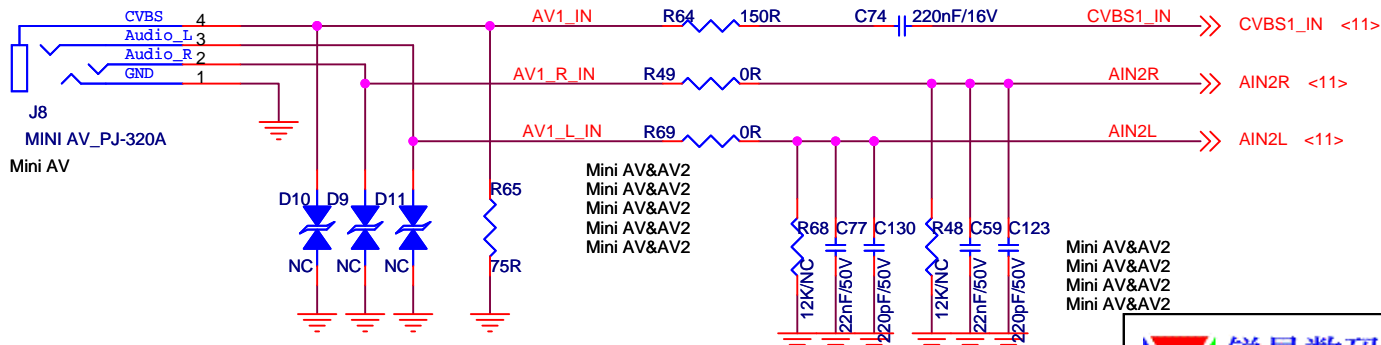
YPbPr Input



CVBS/Audio Input



MINI AV Input



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A

Document Number

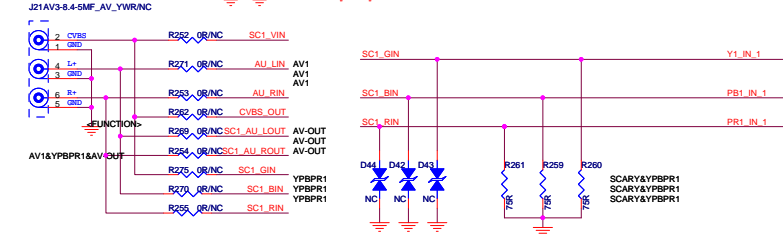
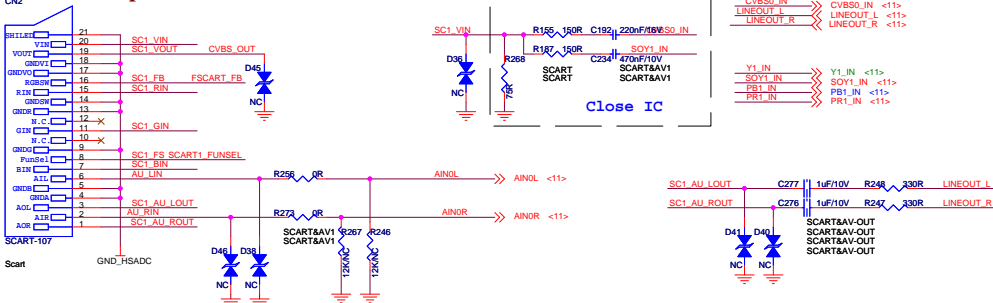
YPbPr & AV

Rev
10

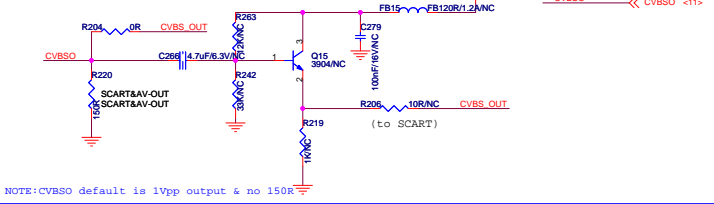
Date: Friday, October 24, 2014

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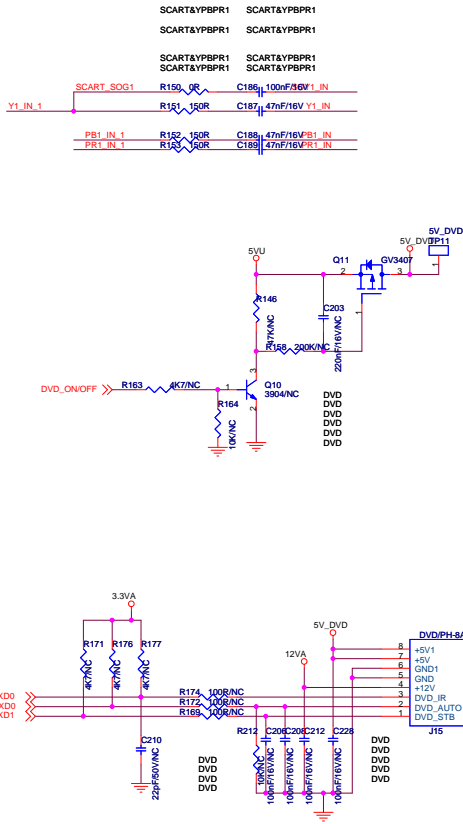
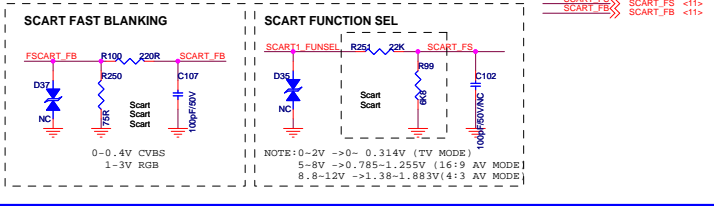
SCART1 Input



CVBS Output



Scart Function switch



TSO CLK M1CLK
TSO VAL R53 22R M1VAL
TSO SYNC R58 22R M1STRT
CI A10/TSO D1 MD10
CI A9/TSO D2 MD11
CI A8/TSO D3 MD12
CI A7/TSO D4 MD13
CI A6/TSO D5 MD14
CI A5/TSO D6 MD15
CI A4/TSO D7 MD16

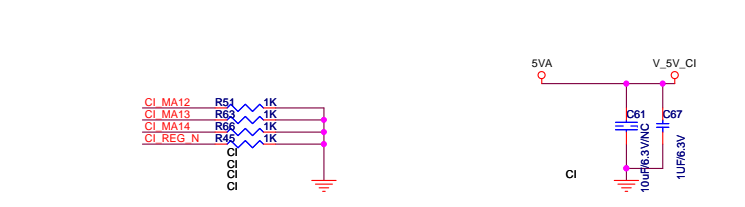
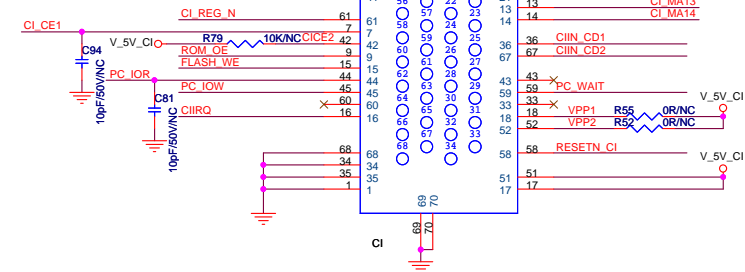
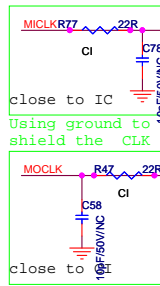
TSI CLK M1CLK
TSI VAL R44 22R M1VAL
TSI SYNC R42 22R M1STRT
TSI D0 MD00
TSI D1 MD01
TSI D2 MD02
TSI D3 MD03
TSI D4 MD04
TSI D5 MD05
TSI D6 MD06
TSI D7 MD07

CI CE R82 22R CI CE1
CI OE R80 22R ROM OE
CI WE R67 22R FLASH WE
CI IORD R78 22R PC IOR
CI IOWR R73 22R PC IOW

CI MD0 CI D0
CI MD1 CI D1
CI MD2 CI D2
CI MD3 CI D3
CI MD4 CI D4
CI MD5 CI D5
CI MD6 CI D6
CI MD7 CI D7

CI MA0 CI A0
CI MA1 CI A1
CI MA2 CI A2
CI MA3 CI A3
CI MA4 CI A4/TSO D7
CI MA5 CI A5/TSO D6
CI MA6 CI A6/TSO D5
CI MA7 CI A7/TSO D4
CI MA8 CI A8/TSO D3
CI MA9 CI A9/TSO D2
CI MA10 CI A10/TSO D1
CI MA11 CI A11/TSO D0

V_5V_CI
PC_WAIT R46 4K7/NC CI WAIT
RESETN_CI CI RST
CIIN_CD1 CIIN_CD2
V_5V_CI
CI_IRQ R80 4K7/NC
CI_IRQ R59 22R CI_IRQ



CI D0 CI D0 <11>
CI D1 CI D1 <11>
CI D2 CI D2 <11>
CI D3 CI D3 <11>
CI D4 CI D4 <11>
CI D5 CI D5 <11>
CI D6 CI D6 <11>
CI D7 CI D7 <11>

CI A0 CI A0 <11>
CI A1 CI A1 <11>
CI A2 CI A2 <11>
CI A3 CI A3 <11>
CI A4/TSO D7 CI A4/TSO D7 <11>
CI A5/TSO D6 CI A5/TSO D6 <11>
CI A6/TSO D5 CI A6/TSO D5 <11>
CI A7/TSO D4 CI A7/TSO D4 <11>
CI A8/TSO D3 CI A8/TSO D3 <11>
CI A9/TSO D2 CI A9/TSO D2 <11>
CI A10/TSO D1 CI A10/TSO D1 <11>
CI A11/TSO D0 CI A11/TSO D0 <11>

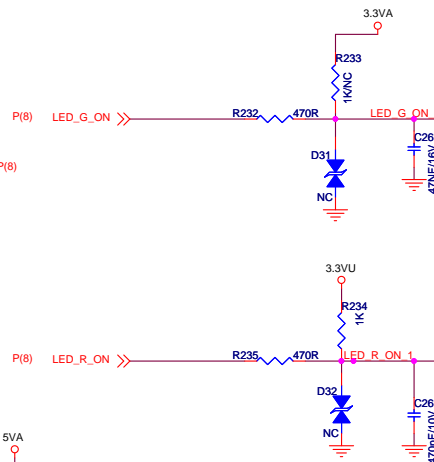
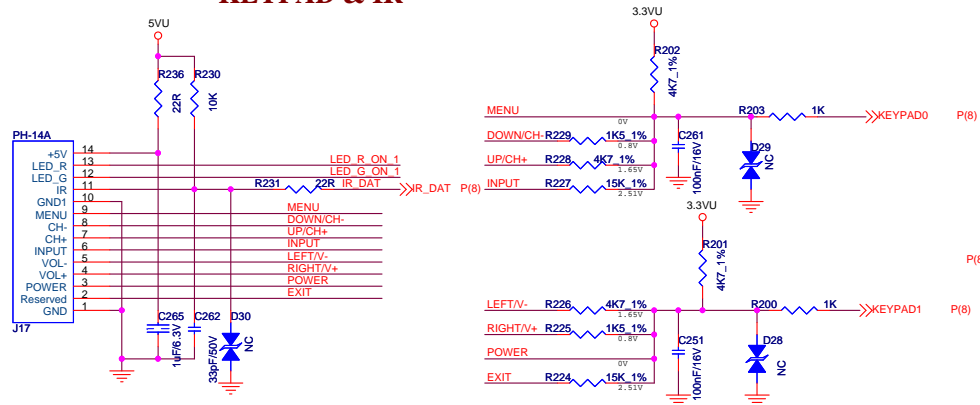
TSO SYNC TSO_SYNC <11>
TSO CLK TSO_CLK <11>
TSO VAL TSO_VAL <11>

TSI D0 TSI D0 <11>
TSI D1 TSI D1 <11>
TSI D2 TSI D2 <11>
TSI D3 TSI D3 <11>
TSI D4 TSI D4 <11>
TSI D5 TSI D5 <11>
TSI D6 TSI D6 <11>
TSI D7 TSI D7 <11>

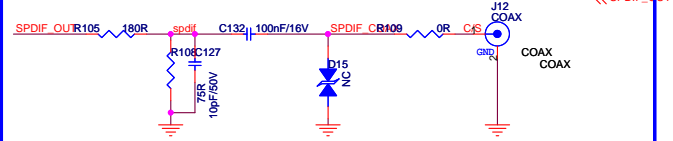
TSI_SYNC TSI_SYNC <11>
TSI_CLK TSI_CLK <11>
TSI_VAL TSI_VAL <11>

CIIN_CD1 CIIN_CD1 <11>
CI WE CI WE <11>
CI OE CI OE <11>
CI IORD CI IORD <11>
CI IOWR CI IOWR <11>
CI WAIT CI WAIT <11>
CI RST CI RST <11>
CI_IRQ CI_IRQ <11>
CI CE CI_CE <11>

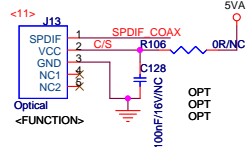
KEYPAD & IR



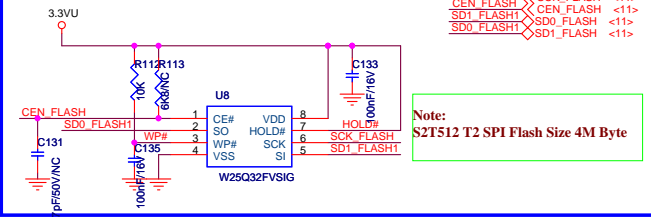
SPDIF OUT



SPDIF/COAX



SPI Flash



Note:
S2T512 T2 SPI Flash Size 4M Byte

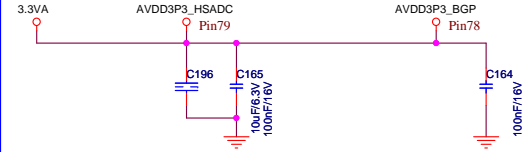
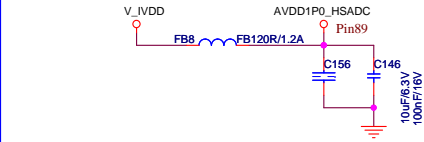
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KEYPAD & IR & FLASH

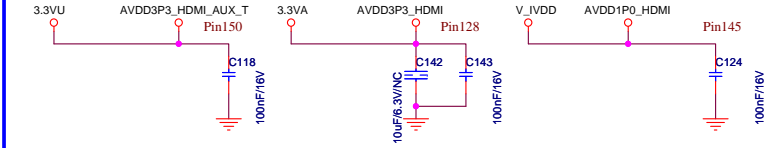
CV512H-A50

ADC POWER/GND (1.0V/3.3V)

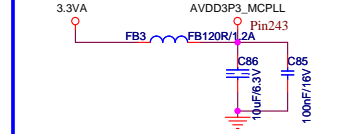
FB for ADC need $R_{dc} < 0.015$, $Z = 120/100\text{MHz}$ $I_{dc} > 3A$
Default part MHC3216S121W



HDMI POWER/GND (1.0V/3.3V)

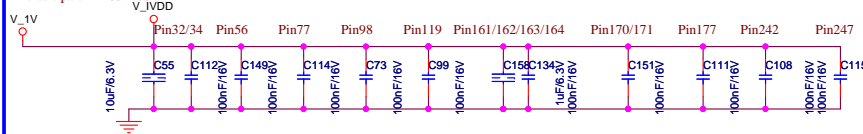


PLL POWER/GND (3.3V)

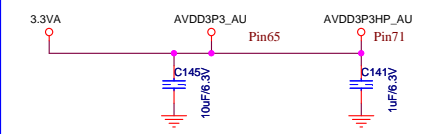


IVDD POWER (1.0V)

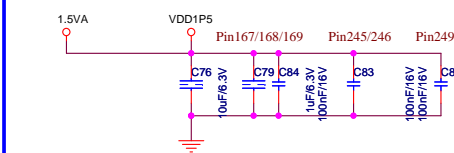
FB for CVDD need $R_{dc} < 0.015$, $Z = 120/100\text{MHz}$ $I_{dc} > 3A$
Default part MHC3216S121W



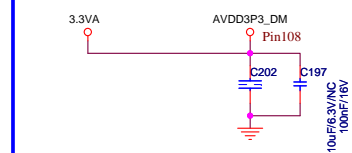
AUDIO POWER/GND (3.3V)



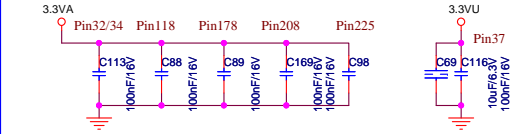
DDR POWER (1.5V)



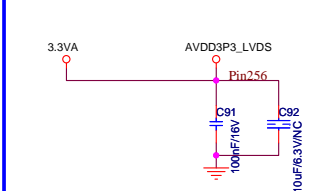
DEMOD POWER/GND (3.3V/1.0V)



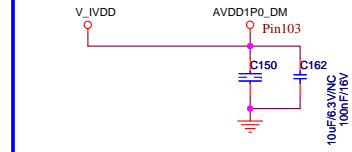
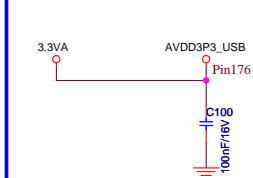
3.3V



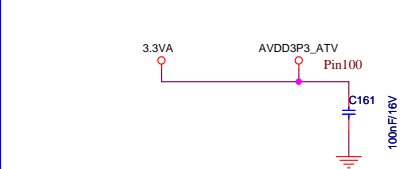
LVDS POWER/GND (3.3V)



USB POWER/GND (3.3V)




ATV POWER/GND (3.3V)



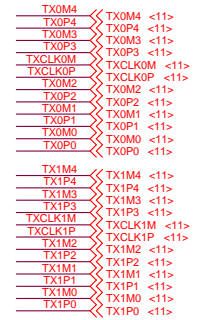
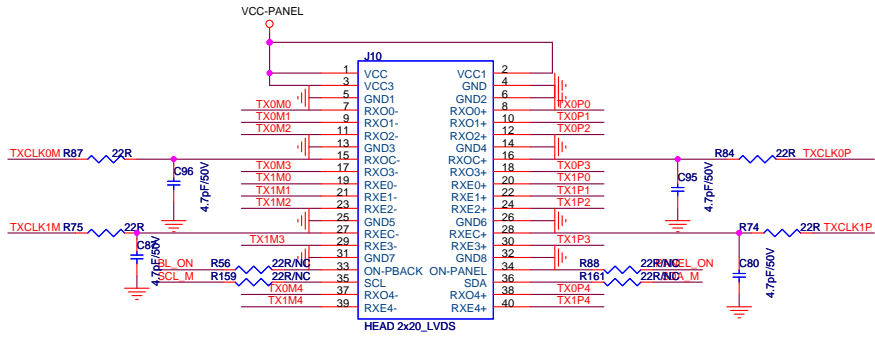
ALL FB except ADC, CVDD : need $R_{dc} < 0.2$, $Z = 300/100\text{MHz}$, $I_{dc} > 500\text{mA}$
Default part : MCB2012S301H



*Short these ground planes on PCB

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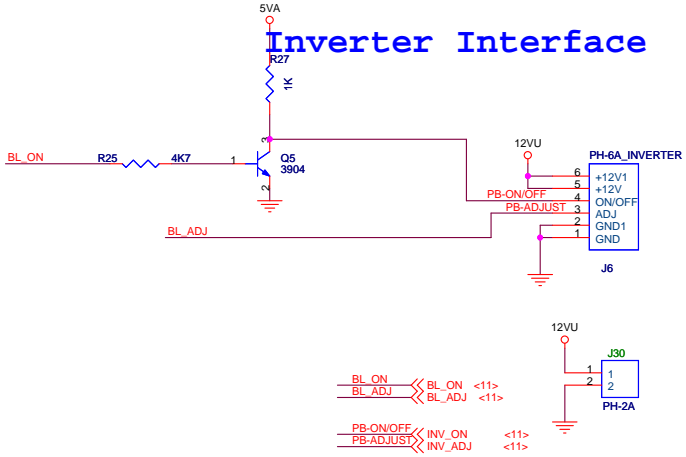
Panel Interface



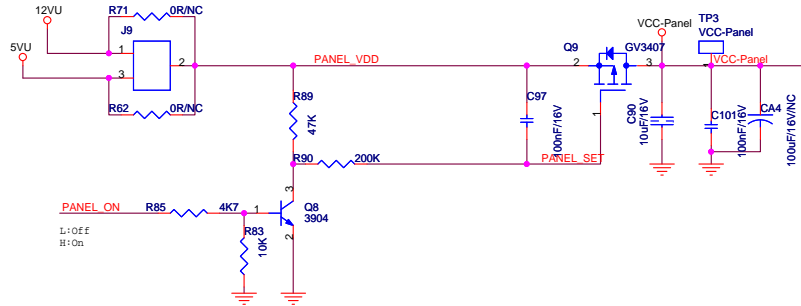
SCL_M <11,15,16>
SDA_M <11,15,16>

PANEL_ON << PANEL_ON <11>

Inverter Interface



Panel Power Control



Title

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Custom

Panel out

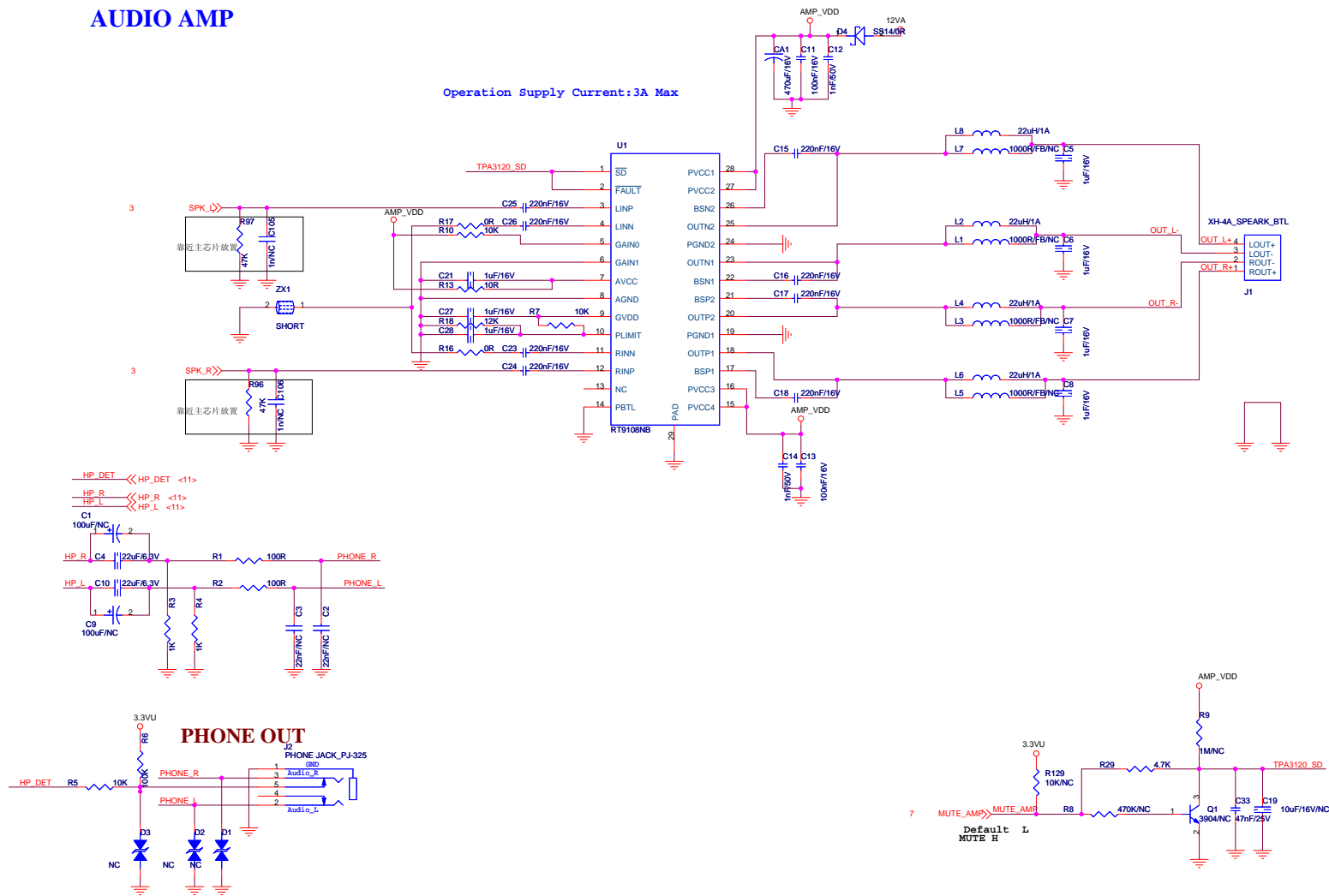
Date: Tuesday, October 28, 2014

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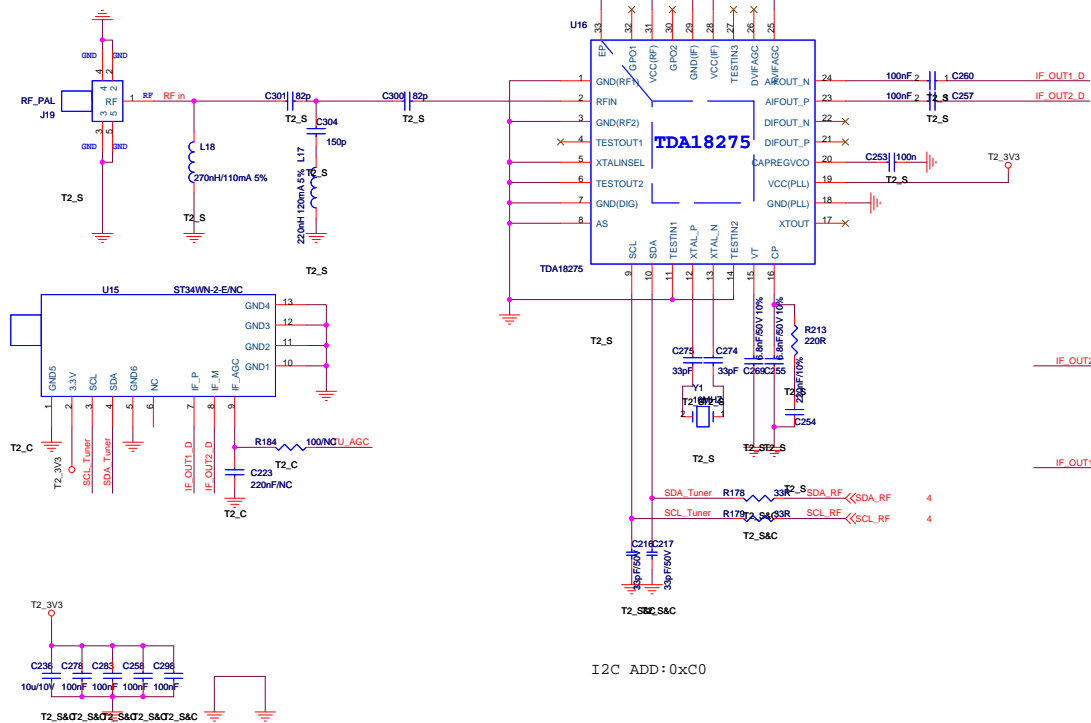
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AUDIO AMP

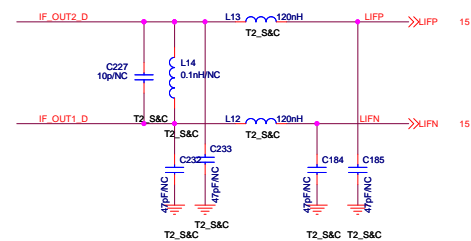
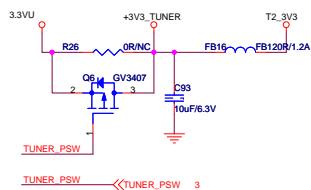
Operation Supply Current:3A Max



TDA18275 Circuit



+3.3V SI-TUNER POWER



```
I2C_ADD:0xC0
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[illegible][illegible]

